

Figure 1

Dreps et al.
AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
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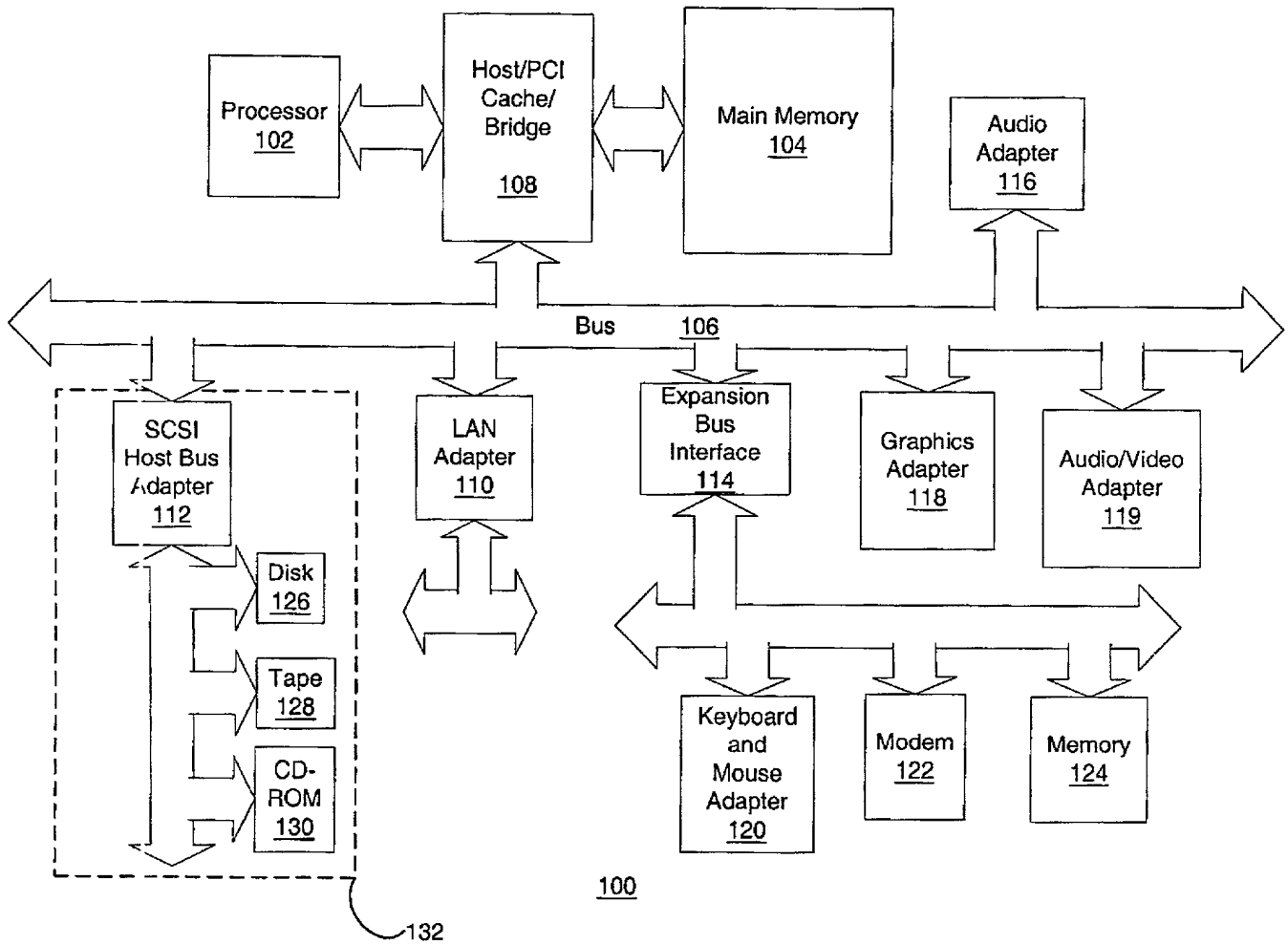


Figure 2

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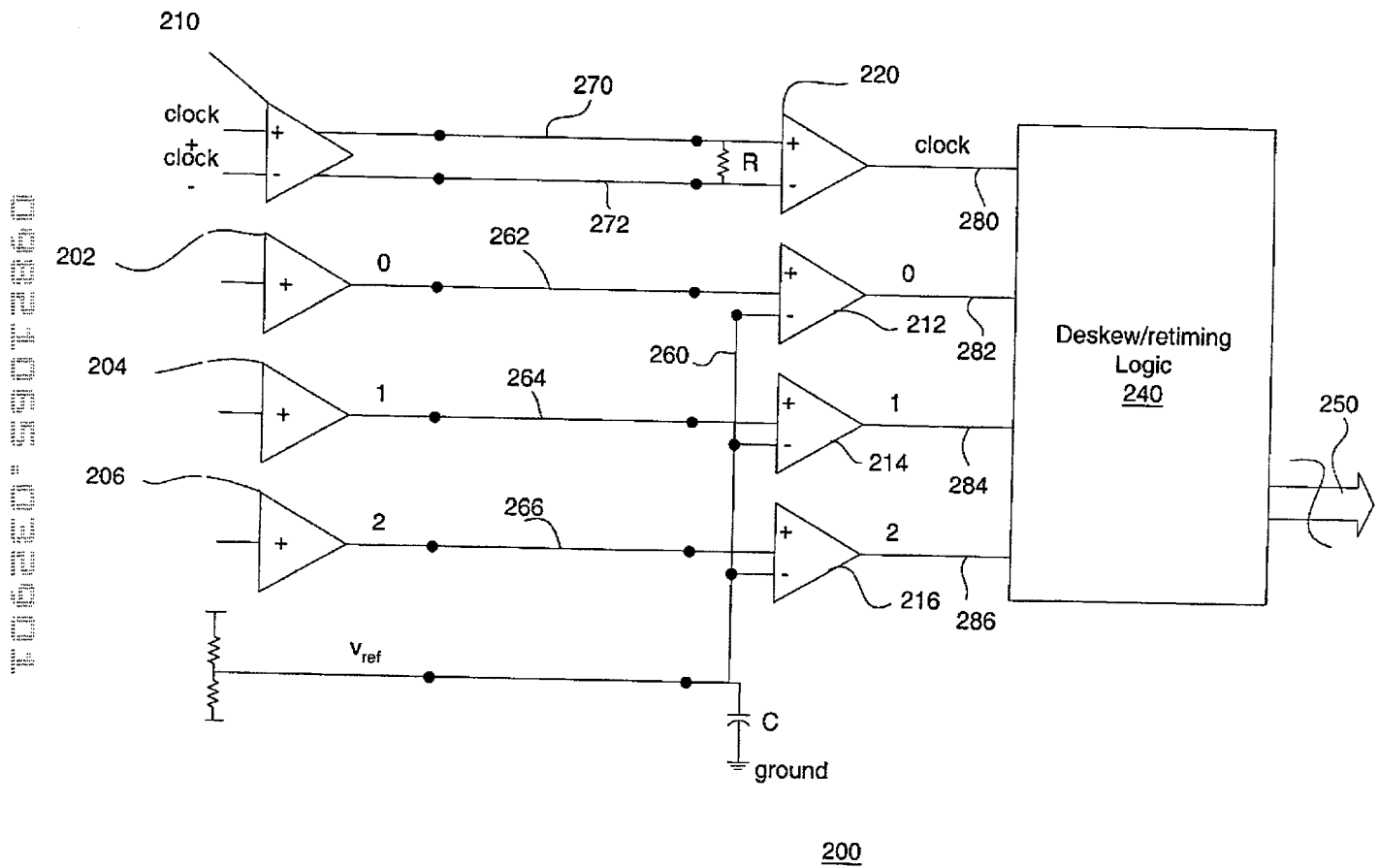


Figure 3

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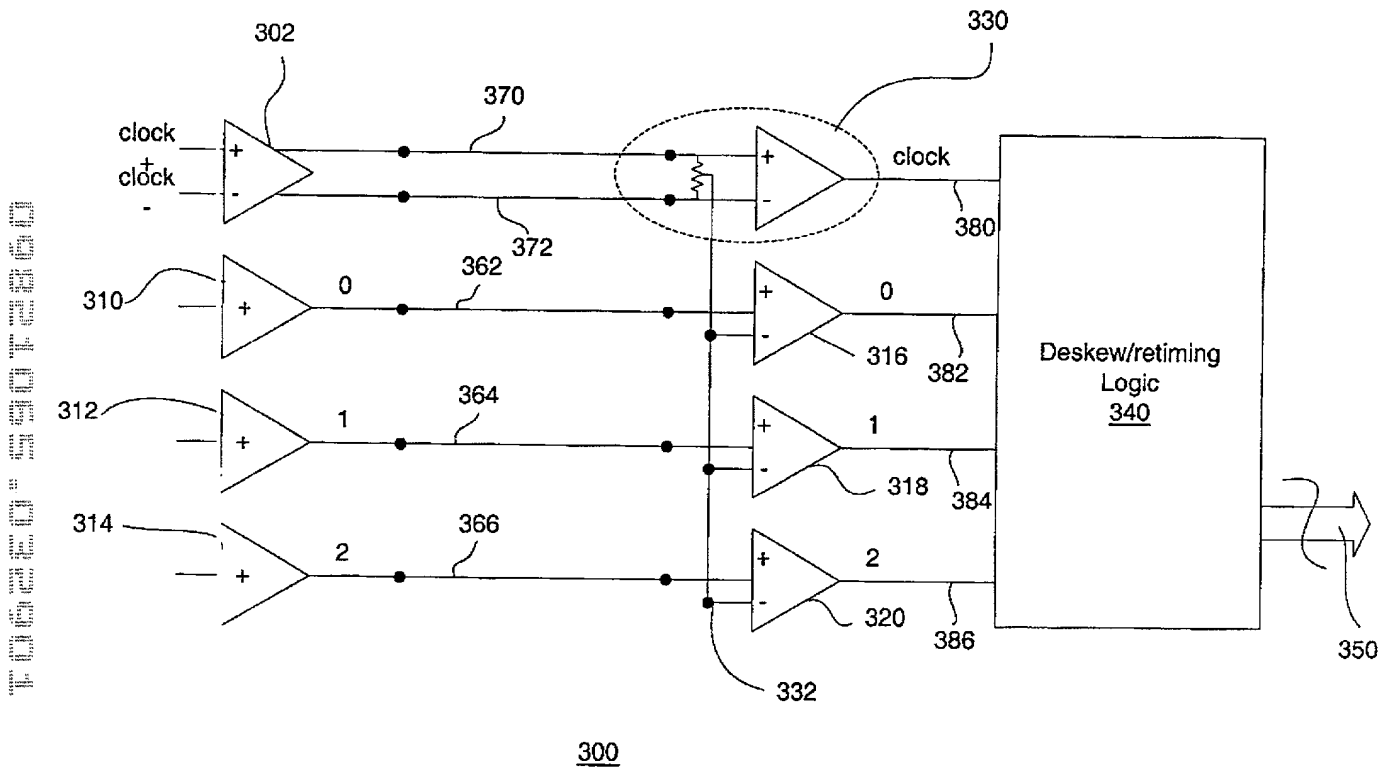


Figure 4

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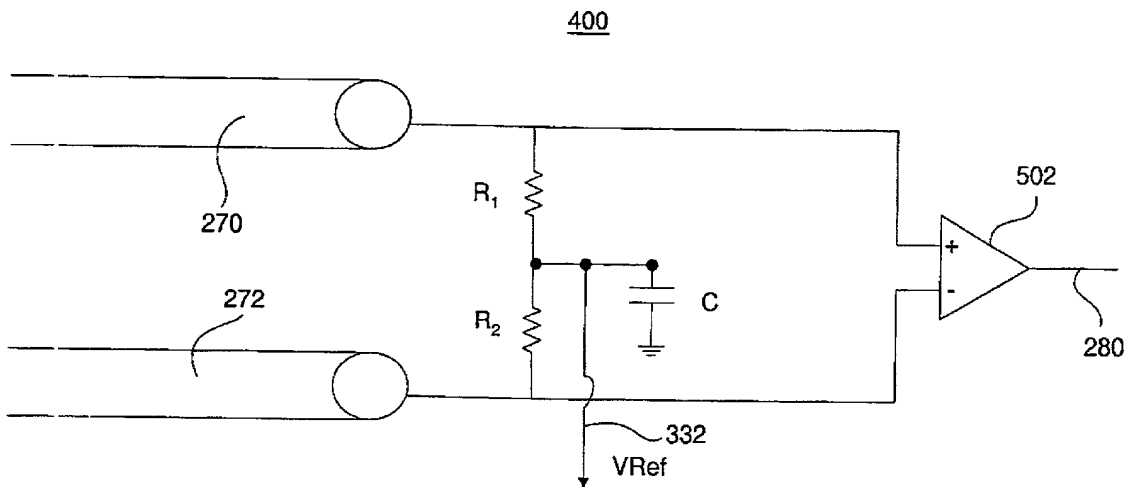
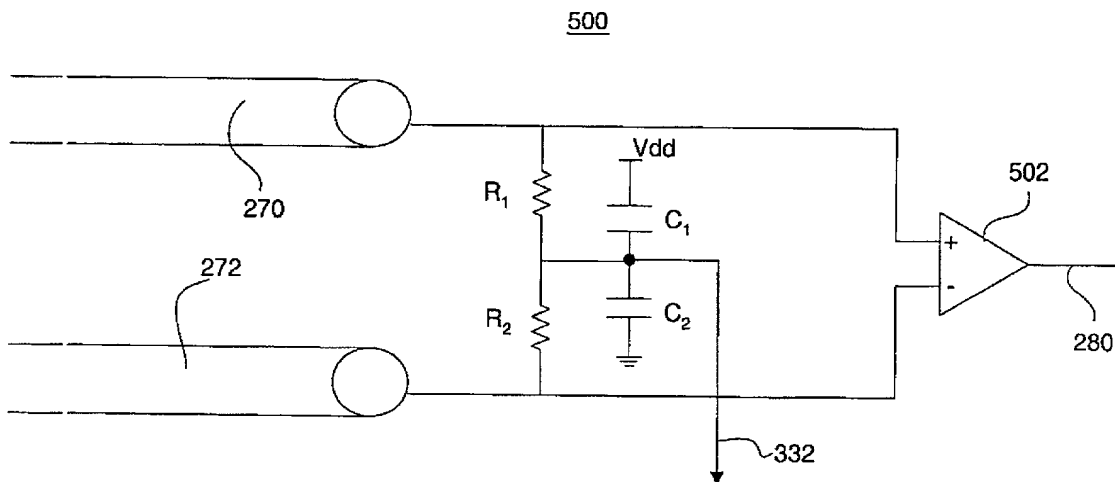


Figure 5

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